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188 C6B3 00          DATA :00
189                *
190                *****
191                * part of READ BLOCK (D340) *
192                *****
193                *
194                * Exit if no loading errors.
195                *
196 C6B4 E3          MPT26   XTHL
197 C6B5 37          STC           CY=1: no error
198 C6B6 E1          LBK30   POP    H
199 C6B7 D1          POP    D
200 C6B8 C1          POP    B
201 C6B9 C9          RET
202                *
203                *****
204                * part of 2E8DE *
205                *****
206                *
207 C6BA CD91CE      SPT02   CALL   :CE91      Go and set screen bits for
208                mode 1
209 C6BD C338E1      JMP    :E138      (2) Pop all, ret.
210                *
211                *
212                * =====
213                *** BANK SWITCHING ***
214                * =====
215                *
216                *
217                *****
218                * MATH. RESTART (RST 4) *
219                *****
220                *
221                * This, and the following routines, switch the
222                * paged banks of ROM. They are entered via
223                * RST x; DATA xx instructions.
224                *
225 C6C0 E1          MARST   POP    H
226 C6C1 F3          DI
227 C6C2 224300      SHLD   :0043      Save HL
228 C6C5 F5          PUSH   PSW
229 C6C6 E1          POP    H
230 C6C7 224100      SHLD   :0041      Save PSW
231 C6CA 2640        MVI    H,:40      ROM bank 1 select bits
232 C6CC 3AD400      LDA    :00D4      Offset of start HW/SW vector
233                *
234                * ROM BANK SWITCHING:
235                *
236                * This routine is generally used by all Restarts
237                * using ROM bank switching.
238                *
239 C6CF E3          MRS10   XTHL
240 C6D0 86          ADD    M           Add entry number
241 C6D1 23          INX    H
242 C6D2 E3          XTHL
243 C6D3 6F          MOV    L,A        Complete entry point address
244 C6D4 3A4000      LDA    :0040      Old bank select port status
245 C6D7 F5          PUSH   PSW        Save it
246 C6D8 E63F        ANI    :3F         Keep other bits
247 C6DA B4          ORA    H           Add new select bits
248 C6DB 324000      STA    :0040      Update memory
249 C6DE 3206FD      STA    :FD06      Update port

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250 C6E1 26E0          MVI   H,:E0
251 C6E3 CDF2C6      CALL  :C6F2      Restore HL, PSW; switch bank
252
253                  * Return from switched bank:
254
255 C6E6 E3           XTHL           Return to old bank
256 C6E7 F5           PUSH  PSW
257 C6E8 7C           MOV   A,H      Get old bank
258 C6E9 324000       STA   :0040     Reinststate memory
259 C6EC 3206FD       STA   :FD06     Re-instate port
260 C6EF F1           POP   PSW
261 C6F0 E1           POP   H
262 C6F1 C9           RET           Return to caller
263
264                  * SWITCH TO ROM BANK:
265
266                  * HL and PSW are restored. On exit, the
267                  * program switches to the selected ROM bank.
268                  * After return from this bank, the program
269                  * continues on C6E6.
270
271 C6F2 E5           MRDCL  PUSH  H
272 C6F3 2A4100       LHLD  :0041
273 C6F6 E5           PUSH  H
274 C6F7 F1           POP   PSW      Restore A,F
275 C6F8 2A4300       LHLD  :0043     Restore H,L
276 C6FB FB           EI
277 C6FC C9           RET           Switch to selected bank
278
279                  *
280                  *****
281                  * SCREEN RESTART (RST 5) *
282                  *****
283                  *
284                  * From SRS10 also used by RST 1.
285                  *
285 C6FD E1           SCRST  POP   H
286 C6FE F3           DI
287 C6FF 224300       SHLD  :0043     Save HL
288 C702 F5           PUSH  PSW
289 C703 3E80         MVI   A,:80     ROM bank 2 select bits
290 C705 E1           SRS10 POP   H
291 C706 224100       SHLD  :0041     Save PSW
292 C709 67           MOV   H,A
293 C70A AF           XRA   A
294 C70B C3CFC6       JMP   :C6CF     Switch to new bank
295
296                  *
297                  *****
298                  * UTILITY/ENCODE (RST 1) *
299                  *****
300                  *
300 C70E E1           UTRST POP   H
301 C70F F3           DI
302 C710 224300       SHLD  :0043     Save HL
303 C713 F5           PUSH  PSW
304 C714 3EC0         MVI   A,:C0     ROM bank 3 select bits
305 C716 C305C7       JMP   :C705     Switch to ROM bank 3
306
307                  *
308                  * =====
309                  *** BASIC HANDLER ***
310                  * =====
311                  *

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